

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of :
: **Chaur-Chin YANG et al.** : Group Art Unit: Unassigned
:
Serial No.: Unassigned : Examiner: Unassigned
:
Filed: August 27, 2003 :
:
For: **FLIP-CHIP PACKAGE**

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner of Patents
BOX PATENT APPLICATION
P.O. Box 1450
Alexandria, Virginia 22313-1450

Sir:

In compliance with the duty of disclosure under 37 CFR 1.56, and 37 CFR 1.97-1.98, the documents listed on the attached form PTO-1449 are hereby made of record in this patent application. Copies of the listed documents are enclosed.

Both cited patents disclose flip-chip semiconductor packages.

As this Information Disclosure Statement is being filed prior to the mailing of the first Official Action in this application, no fee is believed due in order to have the enclosed reference considered by the Examiner and made of record in the application.

Early action on the merits of the application is earnestly solicited.

Respectfully submitted,

By:


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Date: August 27, 2003

FORM PTO 1449 (modified)

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICELIST OF REFERENCES CITED BY APPLICANT(S)
(Use several sheets if necessary)Date Submitted to PTO: **August 27, 2003**ATTY DOCKET NO. **BHT-3183-53**APPLICATION NO. **Unassigned**APPLICANT **Chaur-Chin YANG et al.**FILING DATE **August 27, 2003**GROUP **Unassigned**

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES/NO/ OR ABSTRACT
		457664	10/01/01	Republic of China			
		489496	06/01/02	Republic of China			

OTHER DOCUMENT(S) (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER

DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.